501.25958CV7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. HOTTA, et al

Serial No.: Not yet assigned

Filing Date: November 6, 2003

For: DATA PROCESSING SYSTEM GENERATING CLOCK SIGNAL FROM AN INPUT CLOCK PHASE LOCKED TO THE INPUT CLOCK AND USED FOR CLOCKING LOGIC DEVICES

LETTER CLAIMING RIGHT OF PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 6, 2003

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, applicants hereby claim the right of priority based on:

Japanese Application No. 62/101930
Filed: April 27, 1987

Japanese Application No. 62/181060
July 22, 1987

Certified copies of said application documents were filed in parent application Serial No. 07/184,782, filed April 22, 1988, now U.S. Patent No. 5,133,064, said certified documents filed in the United States Patent and Trademark Office on February 4, 1992. Acknowledgement thereof is respectfully requested.

Respectfully submitted,

Carl I. Brundidge
Registration No. 29,621
ANTONELLI, TERRY, STOUT & KRAUS, LLP

CIB/jdc
Enclosures
703/312-6600

日 本 国 特 許 庁
PATENT OFFICE
JAPANESE GOVERNMENT

付の書類は下記の出願書類の謄本に相違ないことを証明する。
s to certify that the annexed is a true copy of the following application as filed
s Office.

年 月 日
f Application: １９８７年４月２７日

番 号
ion Number: 昭和６２年特許願第１０１９３０号

願 人
t(s): 株式会社日立製作所

１９８８年 ５ 月２７日

特許庁長官
Director-General,
Patent Office 小川邦夫

出証昭 ６３－２６０５７